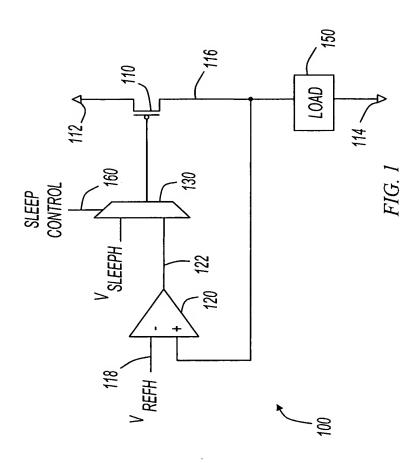


TITLE: REGULATED SLEEP TRANSISTOR APPÀRATUS, METHOD, AND SYSTEM INVENTORS NAME: Peter Hazucha et al.

SHEET SERIAL No.: 10/673,822

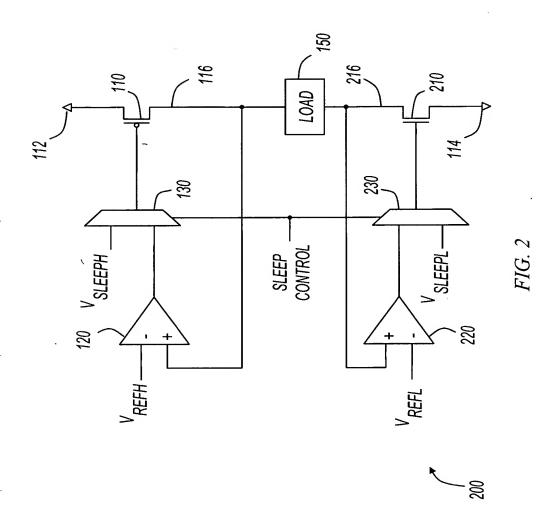
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TITLE: REGULATED SLEEP TRANSISTOR APPÀRATUS, METHOD, AND SYSTEM INVENTORS NAME: Peter Hazucha et al.

SHEET SERIAL No.: 10/673,822

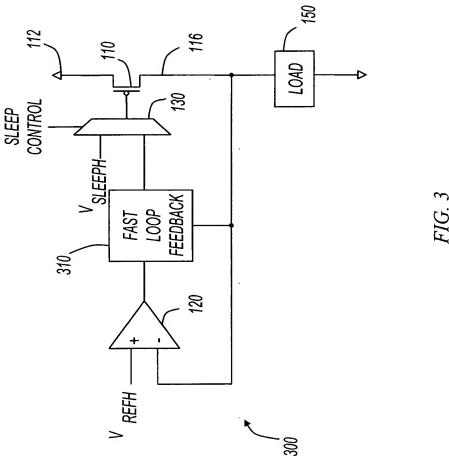
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TITLE: REGULATED SLEEP TRANSISTOR APPÄRATUS, METHOD, AND SYSTEM INVENTORS NAME: Peter Hazucha et al.

SHEET SERIAL No.: 10/673,822

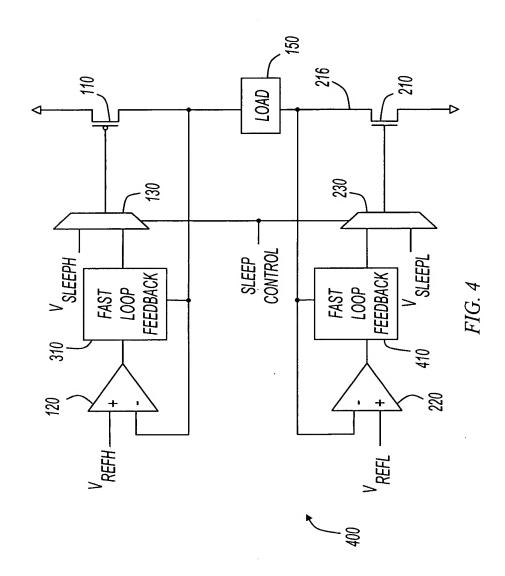
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TITLE: REGULATED SLEEP TRANSISTOR APPÄRATUS, METHOD, AND SYSTEM INVENTORS NAME: Peter Hazucha et al.

SHEET SERIAL No.: 10/673,822

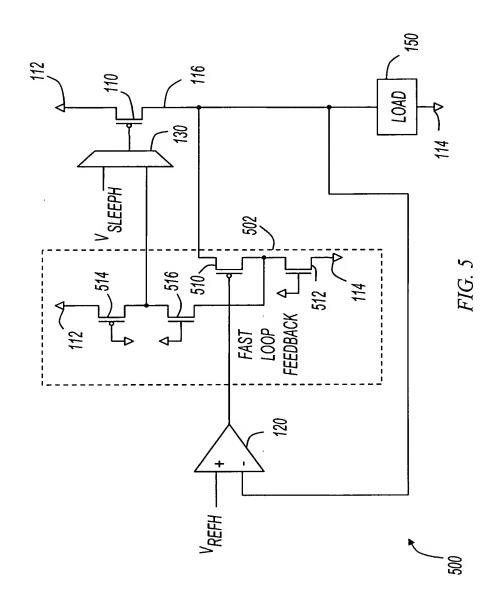
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TITLE: REGULATED SLEEP TRANSISTOR APPARATUS, METHOD, AND SYSTEM INVENTORS NAME: Peter Hazucha et al.

SHEET SERIAL No.: 10/673,822

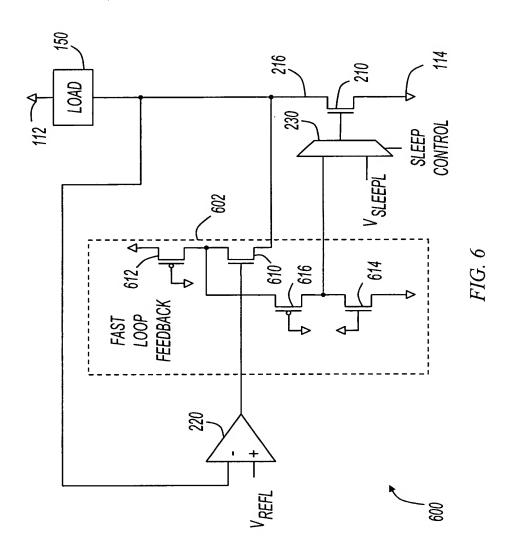
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TITLE: REGULATED SLEEP TRANSISTOR APPARATUS, METHOD, AND SYSTEM INVENTORS NAME: Peter Hazucha et al.

SHEET SERIAL No.: 10/673,822

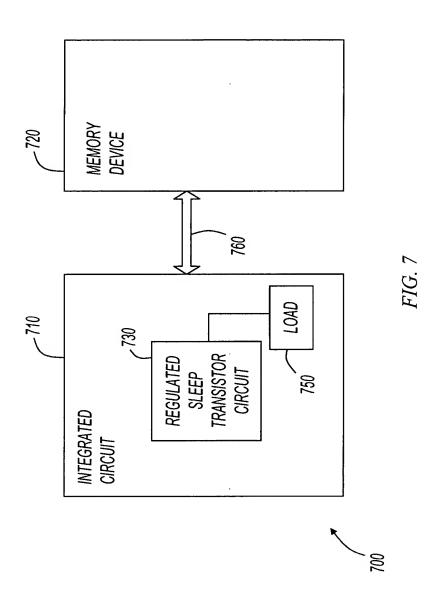
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SHEET SERIAL No.: 10/673,822

REPLACEMENT SHEET



TITLE: REGULATED SLEEP TRANSISTOR APPARATUS, METHOD, AND SYSTEM

INVENTORS NAME: Peter Hazucha et al.

REPLACEMENT SHEET

SERIAL No.: 10/673,822

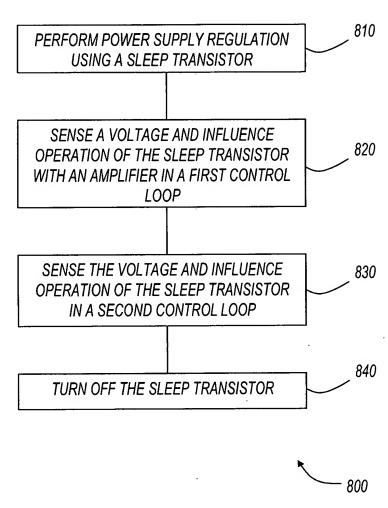


FIG. 8

TITLE: REGULATED SLEEP TRANSISTOR APPAŘAŤUS, MĚTHOD, AND SYSTEM INVENTORS NAME: Peter Hazucha et al.

SHEET SERIAL No.: 10/673,822

· REPLACEMENT SHEET

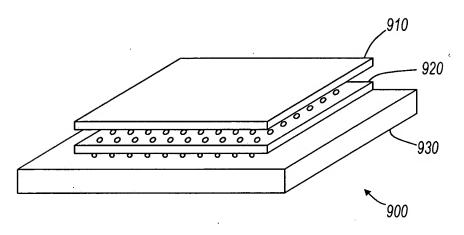


FIG. 9

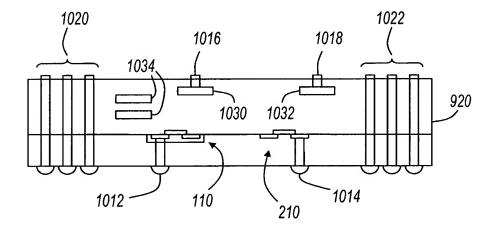


FIG. 10